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APPLICANT

Oğuz Tanrikulu and Sidd Gupta

FILING DATE

October 25, 2000

GROUP

2643

U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

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Technology Center 2600

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
	AL						
	AM						
	AN						
	AO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TPA	AR	Vaidyanathan, P.P., "Multi-rate Systems and Filter Banks," Prentice-Hall (1993)
TPA	AS	Tanrikulu, O. and Kalkan, M., "Design and Discrete Re-optimization of All-pass Based Power Symmetric IIR Filters," <i>Electronics Letters</i> , 32(16): 1458-1460 (1996)
TPA	AT	Valenzuela, R.A and Constantinides, A.G., "Digital Signal Processing Schemes for Efficient Interpolation and Decimation," <i>IEE Proceedings-G, Circuits Devices and Systems</i> , 130(6): 225-235 (1983)
TPA	AU	Tanrikulu, O., "Adaptive Signal Processing Algorithms with Accelerated Convergence and Noise Immunity," Ph.D. Thesis, Imperial College of Science, Technology and Medicine, University of London (1995)
TPA	AV	Mitra, S.K. and Hirano, K., "Digital All-Pass Networks," <i>IEEE Trans. Circuits Sys.</i> , CAS-21(5): 688-700 (1974)
TPA	AW	Tanrikulu, O., et al., "Finite-Precision Design and Implementation of All-Pass Polyphase Networks for Echo Cancellation in Sub-Bands," <i>ICASSP-95, Detroit, USA</i> , 4: 3039-3042 (1995, May)

EXAMINER

Tiffany Harwood

DATE CONSIDERED

2/03/03